

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

1. (cancelled)

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2. (currently amended) A memory unit including a memory device having at least one of a data unit and a semiconductor memory chip having a data unit and a management unit corresponding to said data unit,

wherein said memory device data unit is divided into a plurality of plural memory areas,

wherein said management information unit is capable of controlling separately each of said memory areas for accessing from outside is tabled in using management information corresponding to each of said data unit memory areas,

wherein said management information includes at least an arbitrary one of first control information, second control information, third control information and to fourth control information,

wherein said first control information indicates whether or not prohibiting writing data in a

prescribed~~corresponding~~ memory area of said plural memory areas is prohibited,

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wherein said second control information indicates whether or not prohibiting reading out data from a prescribed~~corresponding~~ memory area of said plural memory areas is prohibited,

wherein said third control information indicates whether or not there is capability ~~being capable~~ of performing simultaneously mirroring for storing data which are written in a prescribed memory area of said plural memory areas in a plurality of places and for storing said data to another memory area at a same time, and

wherein said fourth control information indicates whether or not there is capability ~~being capable~~ of time difference mirroring for storing data which are written in a prescribed memory area of said plural memory areas in plural places and for storing said data to another memory area after a prescribed time has passed from data storing to said prescribed memory area.

3. (currently amended) A memory unit according to claim 2, wherein said management information includes fifth control information, and

wherein said fifth control information indicates ~~for~~
identifying whether or not a memory area which is kept as a
spare area in advance is capable of being used.

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4. (currently amended) A memory unit according to
claim 3, wherein said management information includes sixth
control information, and

wherein said sixth control information indicates for
indicating erasing number of times or rewriting number of
times of a memory area.

5. (currently amended) A memory unit according to
claim 4, ~~wherein~~ further including a memory means device for
storing said management information ~~altogether is included.~~

6. (original) A memory unit according to claim 4,
wherein an area for storing said management information by
each memory area is provided in said plural memory areas.

7. (currently amended) A memory unit according to
claim 6, ~~wherein~~ further including a control means device for
controlling separately said each memory area based on said
management information ~~is included.~~

8. (currently amended) A memory unit according to claim 7, wherein said control ~~means~~device includes a micro processing unit for processing said management information by software.

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9. (currently amended) A memory unit according to claim 7, wherein said control ~~means~~device includes a controlling unit for processing said management information by exclusive hardware.

10-11 (cancelled)

12. (currently amended) ~~A method for controlling a memory device including at least one semiconductor memory chip having a data unit and a management unit corresponding to said data unit,~~

wherein, when said ~~memory device~~data unit is divided into plural memory areas,

~~wherein the method comprises a step for controlling~~said memory device controls accessing separately
~~said memory device by~~for each memory area based on management information that is tabled on said data unit,

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wherein said management information includes an arbitraryby including at least one of first control information, second control information, third control information and to fourth control information,

wherein said first control information indicates whether or not prohibiting writing data in a prescribed corresponding memory area of said plural memory areas is prohibited,

wherein said second control information indicates whether or not prohibiting reading out data from a prescribed corresponding memory area of said plural memory areas is prohibited,

wherein said third control information indicates whether or not there is capability of storing data that are written in a prescribed memory area of said plural memory areas in plural placesand storing said data to another memory area nearly at a same time, and

wherein said fourth control information indicates whether or not there is capability of storing data that are written in a prescribed memory area of said plural memory areas in plural placesand storing said data to another memory area after a prescribed time has passed from said storing data to said prescribed memory area.

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13. (currently amended) ~~A method for controlling a~~
memory device according to claim 12, wherein said
management information further includes fifth control
information for identifying whether or not a memory area is
usable.

14. (currently amended) ~~A method for controlling a~~
memory device according to claim 13, wherein said
management information further includes sixth control
information for indicating erasing number of times and
rewriting number of times of a memory area.

15. (currently amended) ~~A method for controlling a~~
memory device according to claim 14, wherein said method
~~includes a first step having a management information edit~~
~~step capable of editing said management information is~~
capable of being edited in an edit mode, and

wherein said memory device is capable of transferring
to said edit mode by an inputted command

~~identifying whether or not said step of editing said~~
~~management information transits to an edit mode of said~~
~~management information and a second step for editing said~~

B18 ~~management information in an edit mode which is transited~~
~~based on an identifying result at said first step.~~

16-21 (cancelled)
